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| | |
|-----------------------|--------------------|
| Jackie McBride | (Depositor's name) |
| <i>Jackie McBride</i> | (Signature) |
| 4-14-2005 | (Date) |

04/19/2005 MBELETE2 00000119 200668 10026053

01 FC:1501 1400.00 DA
02 FC:1504 300.00 DA

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/026,053 | 12/21/2001 | Mark A. Burns | TI-33380 | 8705 |

TITLE OF INVENTION: PARALLEL INTEGRATED CIRCUIT TEST APPARATUS AND TEST METHOD

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$1700 | 04/14/2005 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|---------------|----------|----------------|
| Nguyen, Jimmy | 2829 | 324-765000 |

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- | | |
|---|----------------------------------|
| 1 | <u>Jacqueline J. Garner</u> |
| 2 | <u>Wade James Brady III</u> |
| 3 | <u>Frederick J. Telecky, Jr.</u> |

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(Authorized Signature)

Wade James Brady III

Reg. No. 32,080

(Date)

4/14/05

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